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Electrical overstress of integrated circuits

Abstract. Common misconceptions regarding electrical overstress (EOS) and the failure characteristics of integrated circuits (ICs) are summarized, analyzed and clarified.

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Electrical overstress in integrated circuits -

Semiconductor devices have a limited ability to sustain electrical overstress (EOS). The device susceptibility to EOS increases as the device is scaled down to

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Electric overstress (eos) and its effects on

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Chapter 4 will discuss electrical overstress (EOS) failure analysis (FA). The chapter will discuss the failure analysis process, and FA tools for printed circuit

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